

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3441	(embed\$ imbed\$ chipset (chip adj set))near7(flash flashrom eeprom reprogrammable erasable firmware)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/28 16:52
L2	138	1 same boot	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/28 15:47
L3	37	2 and(@ad<"19990930" @prad<"19990930" @rlad<"19990930")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/28 16:14
L4	351	(chipset (chip adj set))near7(flash flashrom eeprom reprogrammable erasable firmware)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/28 16:14
L5	135	(4 and(@ad<"19990930" @prad<"19990930" @rlad<"19990930"))not 3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/28 16:57
L6	12352	(embed\$ imbed\$ chipset (chip adj set) asic bridge controller)near7(flash flashrom eeprom reprogrammable erasable)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/28 16:55
L7	5825	(firmware boot bios)near7(flash flashrom eeprom reprogrammable erasable)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/28 16:56
L8	1573	6 and 7	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/28 16:55
L9	4575	(firmware boot bios)near7(upgrad\$ updat\$ reprogram\$)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/28 16:57

L10	502	8 and 9	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/28 16:57
L11	246	(10 and(@ad<"19990930" @prad<"19990930" @rlad<"19990930"))not(5 3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/28 19:31
L12	1330	(glue\$5 asic chipset (chip adj set)((multi\$3 plural\$3)adj(ic integrated chip)))near11(flash flashrom)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/28 19:30
L13	154	7 and 12	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/28 19:30
L14	42	(13 and(@ad<"19990930" @prad<"19990930" @rlad<"19990930"))not(5 3 11)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/28 19:31
S1	2	"20040163081"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/27 17:29
S2	3339	(updat\$4 upgrad\$4)near6(boot firmware)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/27 19:56
S3	2428	boot near8(flash\$ eeprom)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/27 17:33
S4	242	S2 same S3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/27 17:33
S5	92	S4 and(@ad<"19990930" @prad<"19990930" @rlad<"19990930")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/27 19:31

S6	873	(updat\$4 upgrad\$4)near6(boot)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/27 19:31
S7	116	(S3 and S6)not S4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/27 19:31
S8	43	S7 and(@ad<"19990930" @prad<"19990930" @rlad<"19990930")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/27 19:45
S9	2635	(updat\$4 upgrad\$4)near6(firmware)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/27 19:44
S10	174	S3 with firmware	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/27 19:44
S11	25	(S9 and S10)not(S4 S7)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/27 19:45
S12	7	S11 and(@ad<"19990930" @prad<"19990930" @rlad<"19990930")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/28 15:47
S13	51497	(chipset incorporat\$ implement\$ single chip)with(flash\$ eeprom nonvolatile (non adj volatile))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/27 19:55
S14	3233	(boot)near8(flash\$ eeprom nonvolatile (non adj volatile))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/27 20:50
S15	48117	(updat\$4 upgrad\$4 download\$ (down adj load\$4) reprogram\$)near6(boot firmware program)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/27 19:57

S16	714	S13 and S14 and S15	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/27 19:57
S17	368	S16 and(@ad<"19990930" @prad<"19990930" @rlad<"19990930")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/27 19:57
S18	280	S17 not(S4 S7 S11)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/27 20:47
S19	25798	(bridge controller chipset asic)with(flash\$ eeprom nonvolatile (non adj volatile))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/27 21:20
S20	1059	S14 and S19	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/27 20:51
S21	218	S2 and S20	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/27 20:51
S22	107	S21 and(@ad<"19990930" @prad<"19990930" @rlad<"19990930")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/27 21:22
S23	2	"6718407".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/27 20:59
S24	1	"6343617".PN.	USPAT; USOCR	OR	ON	2004/11/27 21:00
S25	1	"6266810".PN.	USPAT; USOCR	OR	ON	2004/11/27 21:01
S26	1	"6202123".PN.	USPAT; USOCR	OR	ON	2004/11/27 21:01
S27	1	"6032221".PN.	USPAT; USOCR	OR	ON	2004/11/27 21:01
S28	1	"5968141".PN.	USPAT; USOCR	OR	ON	2004/11/27 21:01

S29	1	"5805882".PN.	USPAT; USOCR	OR	ON	2004/11/27 21:01
S30	9313	S19 with(embed\$ imbed\$ implement\$ incorporat\$ includ\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/27 21:21
S31	267	S2 and S30	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/27 21:22
S32	110	S31 and(@ad<"19990930" @prad<"19990930" @rlad<"19990930")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/27 21:43
S33	253	S2 near5(\$self own automatic\$ autonomous\$)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/27 21:43
S34	99	S33 and(@ad<"19990930" @prad<"19990930" @rlad<"19990930")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/27 21:43